WHAT IS CLAIMED IS:

1. A multi-channel radio operating with multiple security levels, comprising:

more than one input/output, each input/output corresponding to a security level;

a first common bus coupled to the more than one input/output;

a first set of more than one processor coupled to the common bus, each of the first set of processors corresponding to a security level;

a second set of more than one processors coupled to the first set of processors; and

more than one transceiver, each transceiver being coupled to at least one of the processors of the first set of processors.

2. The multi-channel radio operating with multiple security levels of claim 1, further comprising:

a second common bus coupled to the first set of processors and the second set of processors.

3. The multi-channel radio operating with multiple security levels of claim 2, wherein one of the first processors of the first set of processors encodes information received from one of the input/outputs.

4. The multi-channel radio operating with multiple security levels of claim 3, wherein the second common bus directs the encoded information so that it is received by the intended processor of the second set of processors and not received or understood by other of the processors of the second set of processors.

5. The multi-channel radio operating with multiple security levels of claim 4, wherein the first common bus is an Ethernet packet switching device.

6. The multi-channel radio operating with multiple security levels of claim 4, wherein the second common bus is a PCI bus.

7. A method of transmitting data using a multi-channel radio system configured for use with different security levels, comprising:

receiving an information packet;

routing the information packet to a processor of a first set of processors, each of the first set of processors corresponding to a security level, the routing carried out over a first common bus;

encoding the information packet;

routing the information packet by a second common bus to one of a second set of processors; and

transmitting the information packet from one of the second set of processors over the air.

8. The method of claim 7, wherein the first set of processors are red processing devices.

9. The method of claim 7, wherein the first common bus comprises an Ethernet packet switching device.

10. The method of claim 7, wherein the second common bus comprises a PCI bus.

11. The method of claim 7, wherein the second set of processors are black processing devices.

12. A method of receiving data using a multi-channel radio system configured for use with different security levels, comprising:

receiving an information packet from over the air;

routing the information packet to a processor of a first set of processors;

routing the information packet over a first common bus to one of a second set of processors based on the security level of the information packet;

decoding the information packet; and

routing the information packet by a second common bus to one of a set of outputs, each output corresponding to a security level.

13. The method of claim 12, wherein the first set of processors are black processing devices.

14. The method of claim 12, wherein the second common bus comprises an Ethernet packet switching device.

15. The method of claim 12, wherein the first common bus comprises a PCI bus.

16. The method of claim 12, wherein the second set of processors are red processing devices.

17. A multi-channel radio receiving information of different security levels, comprising:

a first set of processors;

a second set of processors, each of the second set of processors corresponding to a security level; and

a common bus interface coupled between the first set of processors and the second set of processors, the interface configured to isolate processors of the second set of processors from one another based on the information security level.

18. The multi-channel radio of claim 17, wherein the second set of processors comprise red processing devices.

19. The multi-channel radio of claim 17, wherein the common bus interface comprises a PCI bus.

20. The multi-channel radio of Claim 17, wherein the first set of processors comprise black processing devices.